

Dynamic Thermal Control of Silicon Nitride Photonic Integrated Circuits

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Abstract

Multiple heaters in photonic integrated circuits must be controlled accurately to achieve reliable performance of the thermo-optic system. In this paper, we show wavelength stabilization using a system with unique design for silicon nitride circuits.

Keywords; Thermo-optic, dynamic, control, feedback

I. INTRODUCTION

Photonic Integrated Circuits (PIC) are a key technology for the practical realization of optical processing in applications as diverse as microwave photonics to quantum applications [1-3]. PIC using silicon based materials in a thermo-optic system can be fabricated with relatively low cost using Complementary Metal-Oxide Semiconductor (CMOS) technology. However, the use of multiple heaters on a chip is prone to thermal crosstalk and environmental change because of the relatively high thermo-optic coefficient of these materials [4][5], creating undesirable wavelength shifts and affecting system performance.

Prior efforts to resolve thermal sensitivity of a thermo-optic system have focused on differential control techniques [6], Wheatstone bridge with Proportional-Integral Derivative (PID) feedback [7], active feedback [8], mean power monitoring [9], dithering [10], and balance homodyne locking [11]. These all require external or on-chip photodetectors to monitor the traveling light in the resonators. The robustness, loop interaction and control are also hard to analyze and may not yield the best overall control.

In this paper, we demonstrate wavelength stabilization in compact and low loss silicon nitride multi-heaters PIC based on Mach Zehnder Interferometer (MZI) structure. We demonstrate that a single-board low-cost microcontroller-based design can be used to implement a reconfigurable multi-channel heater controller for silicon nitride PIC.

II. PHOTONIC CHIP CHARACTERIZATION

The measurement setup for the feedback control system is shown in Fig. 1. Amplified Spontaneous Emission (ASE) generates a broadband light source in the telecom C-band. The output is controlled with an attenuator, a fiber Inline Polarizer

(IP) and a Polarization Controller (PC). The input power was monitored using a Power Meter (PM) and a 50:50 fiber coupler. The fiber connection (red lines) from the coupler to the chip is via Polarization Maintaining optical Fiber (PMF) pigtailed to the chip. The output was connected to Optical Spectrum Analyzer (OSA) and PM using 99% and 1% coupler for analysis.

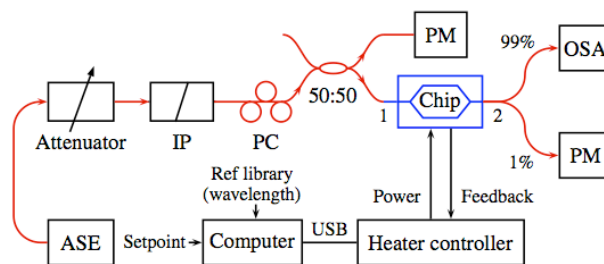


Figure 1. Measurement setup

The silicon nitride circuits were fabricated using double-stripe waveguide technology with LioniX BV [8]. The waveguides comprised of cladding and two strips of Si_3N_4 layers stacked on top of each other with SiO_2 as an intermediate layer. The strips were constructed to be $1.5 \mu\text{m}$ wide, and the Si_3N_4 layers and the SiO_2 intermediate layer were formed to be 170 and 500 nm thick, respectively. This allowed $< 100 \mu\text{m}$ bending radius with a propagation loss of $< 0.2 \text{ dB/cm}$ for TE polarization and single mode operation at 1550 nm with high index contrast.

There are 15 heaters inside the chip, each with a nominal resistance of 600Ω . In this experiment, heaters were connected via electrical wire (black lines) bonded directly to the heater controller as illustrated in Fig. 1. The chip temperature is monitored via an infrared sensor (first sensor), which collects the target chip and ambient temperature. The gap between chip and sensor is around 5 mm. The second sensor is attached on the heatsink plate to monitor overall temperature of the chip. This sensor is using TSICTM306 with T092 package. The photonic chip and sensor setup are shown in Fig. 2(a).

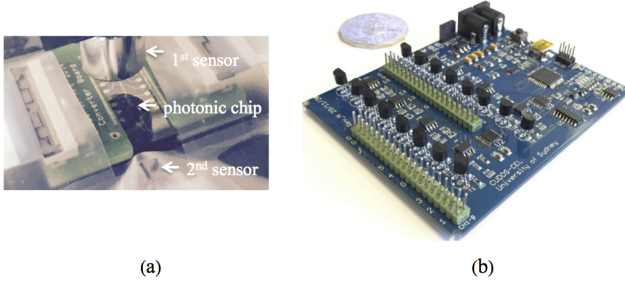


Figure 2. Photonic chip setup (a) and heater controller (b)

The heater controller, as shown in Fig. 2(b), consists of 16 channels outputs, and 15 of them can be used to provide power to the 15 heaters on the photonic chip. The hardware is designed specifically based on open source Arduino using ATmega32u4 with two 16-bit Octal Digital to Analog Converter (DACs), eight dual amplifier and 16 voltage follower circuits integrated onto one board. These electronic circuits can be scaled for more channel output.

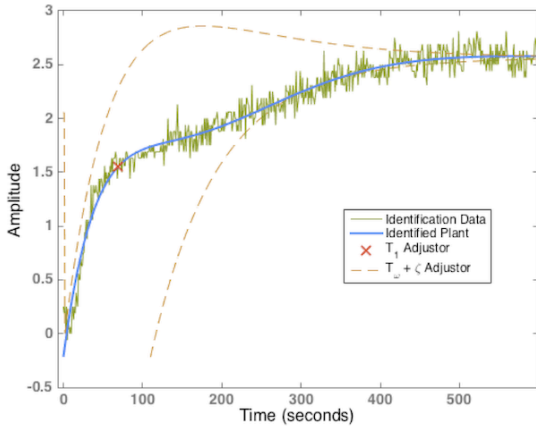


Figure 3. Underdamped pair and real pole plot

III. CONTROL MODEL

The step response model of the plant for Proportional-Integral-Derivative (PID) feedback controller is generated using plant identification system in Matlab and followed by parameterized $H(s)$ with underdamped pair and real pole as shown in Fig. 3 which is described by equation 1. In this model, the configurable parameters include the damping coefficient ζ and the gain K . The other parameters are the first time constant T_1 and the time constant associated with the natural frequency $T\omega$. The estimated parameters for identified plant structure are $K = 5.3132$, $T_1 = 67.285$, $T\omega = 76.3$ and $\zeta = 0.714$.

$$H(s) = \frac{K}{(T_1 s + 1)(T\omega^2 s^2 + 2\zeta T\omega s + 1)} \quad (1)$$

Using PID tuner with balance setting, the rise time and setting time are 108 seconds and 502 seconds respectively. The overshoot is about 3.87 %, peak 1.04 and phase margin 69 deg@0.014 rad/s with stable closed loop.

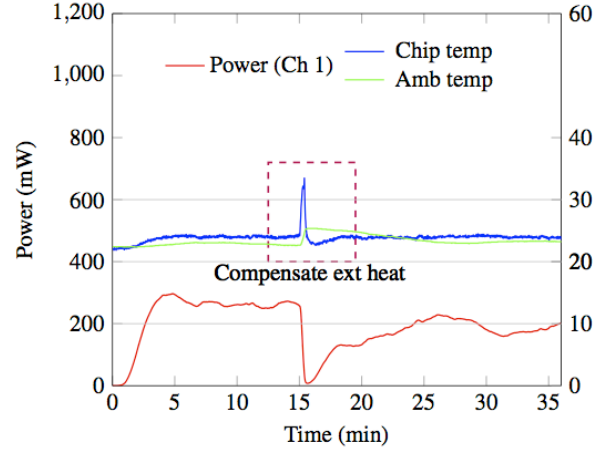


Figure 4. Closed loop experiment with a fan heater disturbance

IV. FEEDBACK CONTROL SYSTEM

For feedback control system, temperature and wavelength data are collected on computer by supplying voltage from 1 to 14 V. The correlation between temperature and wavelength is used as reference for the program. The tuned PID parameters ($K_p = 0.3877$, $K_i = 198.1$, $K_d = 49.52$) are then applied to the feedback control systems. To evaluate the performance of the system, we measure the system stability of closed loop systems.

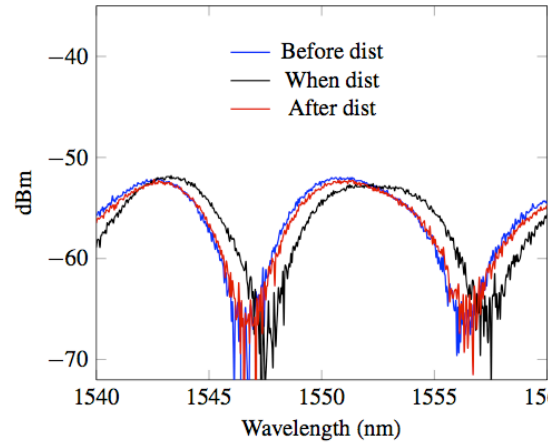


Figure 5. Wavelength stabilization with disturbance

In the closed loop experiment test, channel 2 as the arm of the same MZI on the chip is supplied with 12.9 V. The second step is to set the temperature to 24.2°C. External disturbance is

created using a fan heater. The blower increases the photonic chip temperature to around 34°C as shown in Fig. 4.

Fig. 5 demonstrates wavelength stabilization. From this figure, the wavelength after disturbance is stabilized by the closed loop system control which recovers the wavelength shift to the condition before disturbance.

V. CONCLUSION

In conclusion, feedback control system with reconfigurable multiple channels has been demonstrated. The model can be used as a basic control scheme for more complex system.

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REFERENCES

[1] Roeloffzen *et. al*, "Silicon nitride microwave photonic circuits," *Optics Express*, vol. 21, no. 19, pp. 22937-22961, 2013.

[2] Zhang *et. al*, "Monolithic integration of Si₃N₄ microring filters with bulk CMOS IC through post-backend process," *Photonic Technology Letters, IEEE*, vol. PP, no. 99, pp. 1-1, 2015.

[3] Xiong *et. al*, "Compact and reconfigurable silicon nitride time-bin entanglement circuits," *Optica*, vol. 2, no. 8, pp. 724-727, Aug 2015.

[4] Y. Varshni, "Temperature dependence of the energy gap in semiconductors," *Physica*, vol. 34, no. 1, pp. 149-154, 1967.

[5] M. S. Nawrocka, T. Liu, X. Wang, and R. R. Panepucci, "Tunable silicon microring resonator with wide free spectral range," *Applied Physics Letter*, vol. 89, no. 7, pp. 71110-71110, 2006.

[6] M. Harjanne, M. Kapulainen, T. Aalto, and P. Heimala, "Sub-s switching time in silicon-on-insulator mach-zehnder thermo-optic switch," *Photonics Technology Letters, IEEE*, vol. 16, no. 9, pp. 2039-2041, 2004.

[7] R. Amatya, C. W. Holzwarth, H. I. Smith, and R. J. Ram, "Precision tunable silicon compatible microring filters," *Photonic Technology Letter, IEEE*, vol. 20, no. 20, pp. 1739-1741, 2008.

[8] C. Qiu, J. Shu, Z. Li, X. Zhang, and Q. Xu, "Wavelength tracking with thermally controlled silicon resonators," *Optics Express*, vol. 19, no. 6, pp. 5143-5148, 2011.

[9] K. Padmaraju, J. Chan, L. Chen, M. Lipson, and K. Bergman, "Thermal stabilization of a microring modulator using feedback control," *Optics Express*, vol. 20, no. 27, pp. 27999-28008, 2012.

[10] K. Padmaraju, D. F. Logan, T. Shiraishi, J. J. Ackert, A. P. Knights, and K. Bergman, "Wavelength locking and thermally stabilizing microring resonators using dithering signals," *Lightwave Technology, Journal of*, vol. 32, no. 3, pp. 505-512, 2014.

[11] J. A. Cox, A. L. Lentine, D. C. Trotter, and A. L. Starbuck, "Control of integrated micro-resonator wavelength via balanced homodyne locking," *Optics Express*, vol. 22, no. 9, pp. 11279-11289, 2014.